

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )  
OODAIRA et al. )  
Serial Number: To be assigned )  
Filed: Concurrently herewith )  
For: SEMICONDUCTOR INTEGRATED CIRCUIT, )  
METHOD AND PROGRAM FOR DESIGNING )  
THE SEMICONDUCTOR INTEGRATED CIRCUIT )  
Attorney Docket No. HITA.0440 )

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Sir:

Prior to an examination on the merits, please amend the above-identified application  
as follows: